# Annex X: IEEE Standard Floating-Point Arithmetic

WG14/N514 X3J11/95-115 (Draft 12/21/95)

Jim Thomas Taligent, Inc. 10201 N. DeAnza Blvd. Cupertino, CA 95014-2233 jim\_thomas@taligent.com

This is a proposal for a C9X annex to include the FPCE specification for IEEE implementations.

# Annex X

# (normative) IEEE standard floating-point arithmetic

# X.1 Introduction among belonger SECH of a beginned from a long a factor again to

This annex specifies C language support for the IEEE floating-point standard. The IEEE floating-point standard is specifically IEEE Standard for Binary Floating-Point Arithmetic (ANSI/IEEE 754-1985), also known as Binary floating-point arithmetic for microprocessor systems (IEC 559:1989). IEEE Standard for Radix-Independent Floating-Point Arithmetic (ANSI/IEEE 854-1987) generalizes ANSI/IEEE 754-1985 to remove dependencies on radix and word length. *IEEE* generally refers to the floatingpoint standard, as in IEEE operation, IEEE format, etc. An implementation that defines \_\_\_IEEE\_FP\_\_ conforms to the specification in this annex.

# X.2 Types

The C floating types match the IEEE formats as follows:

- The float type matches the IEEE single format.
- The double type matches the IEEE double format.
- The long double type matches an IEEE extended format<sup>1</sup>, else a non-IEEE extended format, else the IEEE double format.

Any non-IEEE extended format used for the long double type has more precision than IEEE double and at least the range of IEEE double.<sup>2</sup>

<sup>1</sup> Extended is the IEEE standard's double-extended data format. Extended refers to both the common 80bit and quadruple 128-bit IEEE formats.

<sup>&</sup>lt;sup>2</sup> A non-IEEE long double type must provide infinities and NaNs, as its values must include all double values.

## Recommended practice

The long double type matches an IEEE extended format.

[The primary objective of this specification is to facilitate writing portable code that exploits the IEEE standard, including its standardized single and double data formats. Bringing the C data types and the IEEE formats into line advances this objective.

Minimal conformance to the IEEE standard does not require a format wider than single. Standard C (5.2.4.2) requires its double type to be wider than IEEE single, and wider than the minimum IEEE single-extended format. (IEEE single-extended is an optional format intended only for those implementations that don't support double; it has at least 32 bits of precision.) Both Standard C and the IEEE standard would be satisfied if float were IEEE single and double were an IEEE single-extended format with at least 35 bits of precision. However, this specification goes slightly further by requiring double to be IEEE double rather than just a wide IEEE single-extended.

Because of Standard C's bias toward double, extended-based architectures might appear to be better served by associating the C double type with IEEE extended. However, such an approach would not allow standard C types for both IEEE double and single and would go against current industry naming, in addition to undermining this specification's portability goal. The types float\_t and double\_t in <math.h>, are intended to allow effective use of architectures with more efficient, wider formats.

The long double type is not required to be IEEE extended because

- 1. some of the major IEEE architectures for C implementations do not support extended,
- 2. double precision is adequate for a broad assortment of numerical applications, and
- extended is less standard than single or double in that only bounds for its range and precision are specified in IEEE 754.

For certain implementations, non-IEEE extended arithmetic written in software to exploit special hardware features can provide useful extra precision but with significantly better performance than could IEEE extended in software. [???] explains advantages of extended precision.

#### What to do about bibliography references?

This specification accommodates what are expected to be the most important IEEE architectures for general C implementations—see Rationale 5.2.4.2.2.

Specification for a variable-length extended type—one whose width could be changed by the user—was deemed premature. However, not unduly encumbering experimentation and future extensions, for example for variable length extended, is a goal of this specification.

Some narrow-format C implementations, namely ones for digital signal processing, provide only the IEEE single format, possibly augmented by single-extended, which may be narrower than IEEE double or Standard C double, and possibly further augmented by double in software. These non-conforming implementations might generally adopt this specification, though not matching its requirements for types.

One approach would be: match Standard C float with single; match Standard C double with single-extended, else single; and match Standard C long double with double, else single-extended, else single. Then most of this specification could be applied straightforwardly. Users should be clearly warned that the types may not meet expectations.

Another approach would be to refer to a single-extended format as long float and then not recognize any C types not truly supported. This would provide ample warning for programs requiring double. The translation part of porting programs could be accomplished easily with the help of type definitions. In the absence of a double type, most of this

specification for double could be adopted for the long float type. Having distinct types for long float and double, previously synonyms, requires more imagination.]

# X.2.1 Infinities, signed zeros, and NaNs

IEEE formats provide representations for signed infinities, signed zeros, and quiet and signaling NaNs. This specification adopts by reference the IEEE-specified behavior for infinities, signed zeros, and quiet NaNs. This specification does not define the behavior of signaling NaNs.<sup>3</sup> It generally uses the term *NaN* to denote quiet NaNs.

The NAN and INFINITY macros and the nan function in <math.h> provide designations for NaNs and infinities.

[Signaling NaNs are not created by any standard operations or functions, but can be created as bit patterns. Operations that trigger a signaling NaN argument generally return a quiet NaN result provided no trap is taken; neither traps nor any other facility for signaling NaNs is required by the IEEE standard. True support for signaling NaNs implies restartable traps, such as the optional traps specified in the IEEE standard.

This specification fully supports the primary utility of quiet NaNs—"to handle otherwise intractable situations, such as providing a default value for 0.0/0.0" [11] and thereby provide closure for the arithmetic.

Other applications of NaNs may prove useful. Available parts of NaNs have been used to encode auxiliary information, for example about the NaN's origin [4]. Signaling NaNs are good candidates for filling uninitialized storage; and their available parts could distinguish uninitialized floating objects. IEEE signaling NaNs and trap handlers potentially provide hooks for maintaining diagnostic information or for implementing special arithmetics.

However, C support for signaling NaNs, or for auxiliary information that could be encoded in NaNs, is problematic. Trap handling varies widely among implementations. Implementation mechanisms may trigger signaling NaNs, or fail to, in mysterious ways. The IEEE standard requires that NaNs propagate, but not all implementations faithfully propagate the entire contents. And even the IEEE standard fails to specify the contents of NaNs through format conversion, which is pervasive in some C implementation mechanisms. For these reasons this specification does not specify the behavior of signaling NaNs nor the interpretation of NaN significands.

An early draft of the Floating-Point C Extensions part of the X3J11 Numerical C Extensions Technical Report contains specification for signaling NaNs, which could serve as a guide for extensions in support of signaling NaNs.]

# X.3 Operators and functions

C operators and functions provide IEEE required and recommended facilities as listed below. Except where noted, this specification adopts by reference their IEEE-specified behavior.

- The +, -, \*, and / operators provide the IEEE add, subtract, multiply, and divide operations.
- The sqrt function in <math.h> provides the IEEE square root operation.
- The rem function in <math.h> provides the IEEE remainder operation. The remquo function in <math.h> provides the same operation but with additional information.

<sup>&</sup>lt;sup>3</sup> Since NaNs created by IEEE operations are always quiet, quiet NaNs (along with infinities) are sufficient for closure of the arithmetic.

- The rint function in <math.h> provides the IEEE operation that rounds a floating-point number to an integral value (in the same precision). The C nearbyint function in <math.h> provides the similar nearbyinteger function recommended in the Appendix to IEEE standard 854.
- The conversions for floating types provide the IEEE conversions between floating-point precisions.
- The conversions from integral to floating types provide the IEEE conversions from integer to floating point.
- The conversions from floating to integral types provide IEEE-like conversions but always round toward zero.
- The rinttol function in <math.h> provides the IEEE conversions, which honor the directed rounding mode, from floating point to the long integer format. The rinttol function can be used in conjunction with casts to provide IEEE conversions from floating to other integer formats.
- The translation time conversion of floating constants and the strtod, fprintf, and related library functions in <stdlib.h> and <stdlib.h> provide IEEE binary-decimal conversions. The strtold function in <stdlib.h> provides the conv function recommended in the Appendix to IEEE standard 854.
- The relational and equality operators provide IEEE comparisons. The IEEE standard identifies a need for additional comparison predicates to facilitate writing code that accounts for NaNs. The comparison functions (isgreater, isgreaterequal, isless, islessequal, islessgreater, and isunordered) in <math.h> supplement the language operators to address this need. The islessgreater and isunordered macros provide respectively a quiet version of the <> predicate and the unordered predicate recommended in the Appendix to the IEEE standard.
- The feclearexcept, feraiseexcept, and fetestexcept functions in <fenv.h> provide the facility to test and alter the IEEE floating-point exception flags. The fegetexceptflag and fesetexceptflag functions in <fenv.h> provide the facility to save and restore all five status flags at one time. These functions are used in conjunction with the type fexcept\_t and the exception macros (FE\_INEXACT, FE\_DIVBYZERO, FE\_UNDERFLOW, FE\_OVERFLOW, FE\_INVALID) also in <fenv.h>.
- The fegetround and fesetround functions in <fenv.h> provide the facility to select among the IEEE directed rounding modes represented by the rounding direction macros (fe\_tonearest, fe\_upward, fe\_downward, fe\_towardzero) also in <fenv.h>.
- The fegetenv, feholdexcept, fesetenv, and feupdateenv functions in <fenv.h> provide a facility to manage the floating-point environment, comprising the IEEE status flags and control modes.
- The copysign function in <math.h> provides the copysign function recommended in the Appendix to the IEEE standard.
- The unary minus (-) operator provides the minus (-) operation recommended in the Appendix to the IEEE standard.

- The scalb function in <math.h> provides the scalb function recommended in the Appendix to the IEEE standard.
- The logb function in <math.h> provides the logb function recommended in the Appendix to the IEEE standard, but following the newer specification in IEEE 854.
- The nextafterf, nextafterd, and nextafter1 functions in <math.h> provide the nextafter function recommended in the Appendix to the IEEE standard (but with a minor change to better handle signed zeros). The nextafter function in <math.h> provides the same function but in a manner reflecting the implementation's evaluation method.
- The isfinite macro in <math.h> provides the finite function recommended in the Appendix to the IEEE standard.
- The isnan macro in <math.h> provides the isnan function recommended in the Appendix to the IEEE standard.
- The signbit macro and the fpclassify macro, used in conjunction with the number classification macros (fp\_nan, fp\_infinite, fp\_normal, fp\_subnormal, fp\_zero), in <math.h>provide the facility of the class function recommended in the Appendix to the IEEE standard (except that fpclassify does not distinguish signaling from quiet NaNs).

# X.4 Floating to integral conversion

If the floating value is infinite or NaN or if the integral part of the floating value exceeds the range of the integer type, then the invalid exception is raised and the resulting value is unspecified. Whether conversion of nonintegral floating values whose integral part is within the range of the integer type raises the inexact exception is unspecified.<sup>4</sup>

# X.5 Binary-decimal conversion

Conversion from the widest supported IEEE format to decimal with DECIMAL\_DIG digits and back is the identity function.<sup>5</sup>

Conversions involving its IEEE formats follow the recommended practice in 6.1.3.1, 7.9.6.1, 7.10.1.4, 7.16.2.1, and 7.16.4.1.1. In particular, conversion between any supported IEEE format and decimal with **DECIMAL\_DIG** or fewer significant digits is correctly rounded.

<sup>&</sup>lt;sup>4</sup> IEEE 854, but not 754, directly specifies that floating-to-integral conversions raise the inexact exception for nonintegral in-range values. In those cases where it matters, library functions can be used to effect such conversions with or without raising the inexact exception. See rint, rinttol, and nearbyint in smath.

<sup>&</sup>lt;sup>5</sup> If the minimum-width IEEE 754 extended format (64 bits of precision) is supported, DECIMAL\_DIG must be at least 21. If IEEE 754 double (53 bits of precision) is the widest IEEE format supported, then DECIMAL\_DIG must be at least 17. (By contrast, LDBL\_DIG and DBL\_DIG are 19 and 15, respectively, for these formats.)

[The IEEE standard requires perfect rounding for a large though incomplete subset of decimal conversions. This specification goes beyond the IEEE standard by requiring perfect rounding for all decimal conversions, involving DECIMAL\_DIG or fewer decimal digits and a supported IEEE format, because practical methods are now available. Although not requiring correct rounding for arbitrarily wide decimal numbers, this specification is sufficient in the sense that it ensures that every internal numeric value in an IEEE format can be determined as a decimal constant.]

# X.6 Contracted expressions

A contracted expression treats infinities, NaNs, signed zeros, subnormals, and the rounding directions in a manner consistent with the basic arithmetic operations covered by the IEEE standard.

## Recommended practice

A contracted expression raises exceptions in a manner generally consistent with the basic arithmetic operations. A contracted expression delivers the same value as its uncontracted counterpart, else is correctly rounded (once).

# X.7 Environment

The floating-point environment defined in <fenv.h> includes the IEEE exception status flags and directed-rounding control modes. It includes also IEEE dynamic rounding precision and trap enablement modes, if the implementation supports them.<sup>6</sup>

[The exception flags—invalid, overflow, underflow, divide-by-zero, and inexact—can be queried at execution time to determine whether a floating-point exception has occurred since the beginning of execution or since its flag was explicitly cleared. (The flags are sticky.)

The rounding direction modes—to-nearest, toward-zero, upward (toward  $+\infty$ ), and downward (toward  $-\infty$ )—can be altered at execution time to control the rounding direction for floating-point operations.

The IEEE standard prescribes rounding precision modes as a means for a system whose results are always double or extended to shorten the precision of its results, in order to mimic systems that deliver results to single or double precision. An implementation of C can meet this goal in any of the following ways:

- 1. By supporting the method of evaluating expressions to their semantic type.
- 2. By providing translation options to shorten results by rounding to IEEE single or double precision.
- 3. By providing functions to set and get dynamic rounding-precision modes which shorten results by rounding to IEEE single or double precision. Functions fesetprec and fegetprec and macros FE\_FLTPREC, FE\_DBLPREC, and FE\_LDBLPREC, analogous to the functions and macros for the rounding direction modes, would serve the purpose.

This specification does not include a portable interface for precision control because the IEEE standard is ambivalent on whether it intends for precision control to be dynamic (like the

<sup>&</sup>lt;sup>6</sup> This specification does not require dynamic rounding precision nor trap enablement modes.

rounding direction modes) or static. Indeed, some floating-point architectures provide control modes, suitable for a dynamic mechanism, and others rely on instructions to deliver single-and double-format results, suitable only for a static mechanism.

The traps recommended by the IEEE standard require modes for enabling and disabling.]

# X.7.1 Environment management

The IEEE standard requires that floating-point operations implicitly raise exception status flags, and that rounding control modes can be set explicitly to affect result values of floating-point operations. When the state for the fenv\_access macros (defined in <fenv.h>) is on, these changes to the floating-point state are treated as side effects which respect sequence points.<sup>7</sup>

#### X.7.2 Translation

During translation the IEEE default modes are in effect:

- The rounding direction mode is rounding to nearest.
- The rounding precision mode (if supported) is set so that results are not shortened.
- Trapping or stopping (if supported) is disabled on all exceptions.

# Recommended practice

The implementation provides a non-fatal disgnostic for each translation-time floating-point exception, other than inexact.<sup>8</sup>

[An implementation is not required to provide a facility for altering the modes for translation-time arithmetic, or for making exception flags from the translation available to the executing program. The language and library provide facilities to cause floating-point operations to be done at execution time, when they can be subjected to varying dynamic modes and their exceptions detected. The need does not seem sufficient to require similar facilities for translation.]

# X.7.3 Execution

At program startup the floating-point environment is initialized as prescribed by the IEEE standard:

- All exception status flags are clear.
- The rounding direction mode is rounding to nearest.

<sup>7</sup> If the state for the fenv\_access macros is off, the implementation is free to assume the modes will be the default ones and the flags will not be tested, which allows certain optimizations—see X.8.

As floating constants are converted to appropriate internal representations at translation time, their conversion is subject to default rounding modes and raises no execution-time exceptions (even when the state of fenv\_access in on). Library functions, for example strtod, provide execution-time conversion of numeric strings.

- The dynamic rounding precision mode (if supported) is set so that results are not shortened.
- Trapping or stopping (if supported) is disabled on all exceptions.

# X.7.4 Constant expressions

An arithmetic constant expression of floating type, other than one in an initializer for an object that has static storage duration or in an initializer list for an object that has aggregate or union type, is evaluated (as if) during execution. As execution-time evaluation, it is affected by any operative modes and raises exceptions as required by the IEEE standard (provided the state for fenv\_access is on).

## Example

For the aggregate and static initializations, the division is done at translation time, raising no (execution-time) exceptions. On the other hand, for the two automatic scalar initializations the invalid division occurs at execution time.

[A previous approach allowed translation-time constant arithmetic, but empowered the unary + operator, when applied to an operand, to inhibit translation-time evaluation of constant expressions.]

#### X.7.5 Initialization

All computation for automatic scalar initialization is done (as if) at execution time. As execution-time evaluation, it is affected by any operative modes and raises exceptions as required by the IEEE standard (provided the state for fenv\_access is on). All computation for initialization of objects that have static storage duration or that have aggregate or union type is done (as if) at translation time.

Where the state for fenv\_access is on, results of inexact expressions like 1.0/3.0 are affected by rounding modes set at execution time, and expressions such as 0.0/0.0 and 1.0/0.0 generate execution time exceptions. The programmer can achieve the efficiency of translation-time evaluation through static initialization, such as

const static double one\_third = 1.0/3.0;

## Example

```
#include <fenv.h>
fenv_access_on
void f(void)
   float u[] = { 1.1e75 };
                                   /* does not raise exceptions */
   static float v = 1.1e75;
                                   /* does not raise exceptions */
   float w = 1.1e75;
                                   /* raises exceptions */
   double x = 1.1e75;
                                   /* may raise exceptions */
                                   /* may raise exceptions */
   float y = 1.1e75f;
                                   /* does not raise exceptions */
   long double z = 1.1e75;
   /* . . . */
}
```

The aggregate and static initializations of u and v raise no (execution-time) exceptions because their computation is done at translation time. The automatic initialization of w requires an execution-time conversion to float of the wider value 1.1e75, which raises exceptions. The automatic initializations of x and y entail execution-time conversion; however, in some expression evaluation methods, the conversions is not to a narrower format, in which case no exception is raised. The automatic initialization of z entails execution-time conversion, but not to a narrower format, so no exception is raised. Note that the conversions of the floating constants 1.1e75 and 1.1e75f to their internal representations occur at translation time in all cases.

This specification breaks the property that automatic aggregate or union intialization is equivalent to assignment. Changing it so that such initialization has execution-time semantics preserves this property, at the cost of more execution-time computation. The intialization could still be done at translation-time provided the state for fenv\_access were off. This change would also make the specification more suitable for C++.

[This specification does not suit C++, whose static and aggregate initializers need not be constant. Specifying translation-time semantics for initialization of non-local statics with constant-expression initializers, and execution-time semantics for all other floating-point arithmetic, would be consistent with C++ and, given the fenv\_access mechanism, still would allow the bulk of constant arithmetic to be done, in actuality, at translation time.]

# X.7.6 Changing the environment

Operations defined in 6.3 and functions and macros defined for the standard libraries change flags and modes just as indicated by their specification (including conformance to the IEEE standard). They do not change flags or modes (so as to be detectable by the user) in any other cases.

If the argument to the feralseexcept function in <fenv.h> represents IEEE valid coincident exceptions for atomic operations—namely overflow and inexact, or underflow and inexact—then overflow or underflow is raised before inexact.

<sup>10</sup> Use of float\_t and double\_t variables increases the likelihood of translation-time computation. For example, the automatic initialization

double\_t x = 1.1e75; could be done at translation time, regardless of the expression evaluation method.

[IEEE operations order exceptions this way.]

# X.8 Optimization

This section identifies code transformations that might subvert IEEE-specified behavior, and others that do not.

#### X.8.1 "Global" transformations

Floating-point arithmetic operations and external function calls may entail side effects which optimization must honor, at least where the state for fenv\_access is on. The flags and modes in the floating-point environment may be regarded as global variables; floating-point operations (+, \*, etc.) implicitly read the modes and write the flags.

Concern about side effects may inhibit code motion and removal of seemingly useless code. For example, in

```
#include <fenv.h>
fenv_access_on
void f(double x)
{
    /*...*/
    for (i = 0; i < n; i++) x + 1;
    /*...*/
}</pre>
```

x + 1 might raise exceptions, so cannot be removed. And since the loop body might not execute (maybe  $0 \ge n$ ), x + 1 cannot be moved out of the loop. (Of course these optimizations are valid if the implementation can rule out the nettlesome cases.)

This specification does not require support for trap handlers that maintain information about the order or count of exceptions. Therefore, between function calls exceptions need not be precise: the actual order and number of occurrences of exceptions (>1) may vary from what the source code expresses. Thus the preceding loop could be treated as

```
if (0 < n) x + 1;
```

# X.8.2 Expression transformations

- Although similar transformations involving inexact constants generally do not yield numerically equivalent expressions, if the constants are exact then such transformations can be made on IEEE machines and others that round perfectly.
- 1 \* x, x / 1  $\rightarrow$  x The expression 1 \* x and x, and x / 1 and x are equivalent (on IEEE machines, among others). 11
- $x y \leftrightarrow x + (-y)$  The expressions x y, x + (-y), and (-y) + x are equivalent (on IEEE machines, among others).

<sup>11</sup> Strict support for signaling NaNs—not required by this specification—would invalidate these and other transformations that remove arithmetic operators.

$x - y \leftrightarrow -(y - x)$	The expressions $\mathbf{x} - \mathbf{y}$ and $-(\mathbf{y} - \mathbf{x})$ are not equivalent because $1 - 1$ is $+0$ but $-(1 - 1)$ is $-0$ (in the default rounding direction). 12
$x - x \rightarrow 0.0$	The expressions x - x and 0.0 are not equivalent if x is a NaN or infinite.
0 * x \rightarrow 0.0	The expressions 0 * x and 0.0 are not equivalent if x is a NaN or infinite.
$x + 0 \rightarrow x$	The expressions $x + 0$ and $x$ are not equivalent if $x$ is -0, because (-0) + (+0) yields +0 (in the default rounding direction), not -0.
$x - 0 \rightarrow x$	$(+0)$ - $(+0)$ yields -0 when rounding is downward (toward - $\infty$ ), but +0 otherwise, and $(-0)$ - $(+0)$ always yields -0; so, if the state for fenv_access is off, promising default rounding, then the

 $-\mathbf{x} \leftrightarrow \mathbf{0} - \mathbf{x}$ 

The expressions -x and 0 - x are not equivalent if x is +0, because -(+0) yields -0, but 0 - (+0) yields +0 (unless rounding is downward).

implementation can replace x - 0 by x, even if x might be zero.

# X.8.3 Relational operators

 $x = x \rightarrow false$  The statement x = x is true if x is a NaN.

 $x == x \rightarrow true$  The statement x == x is false if x is a NaN.

Though numerically equal, these expressions are not equivalent because of side effects when x or y is a NaN and the state for fenv\_access is on. This transformation, which would be desirable if extra code were required to cause the invalid exception for unordered cases, could be performed provided the state for fenv\_access is off.

The sense of relational operators must be maintained. This includes handling unordered cases as expressed by the source code.

# Example

is not equivalent to

 $1/(1/(\pm\infty))$  is  $\pm\infty$  complex\_conjugate(sqrt(z)) is sqrt(complex\_conjugate(z))

<sup>12</sup> The IEEE standard prescribes a signed zero to preserve mathematical identities across certain discontinuities. Examples include

nor to

```
if (isgreaterequal(a, b)) g(); else f(); /* calls f without raising invalid if a and b are unordered */
```

nor, unless the state of fenv\_access is off, to

but is equivalent to

if (!(a < b)) g(); else f();

#### X.8.4 Constant arithmetic

The implementation must honor exceptions raised by execution-time constant arithmetic wherever the state for fenv\_access is on. (See X.7.4 and X.7.5.) An operation on constants that raises no exception can be folded during translation, except, if the state for fenv\_access is on, a further check is required to assure that changing the rounding direction to downward does not alter the sign of the result, 13 and implementations that support dynamic rounding precision modes must assure further that the result of the operation raises no exception when converted to the semantic type of the operation.

# X.9 < math.h >

This subclause contains specification of <math.h> facilities that is particularly suited for IEEE implementations.

The Standard C macro **HUGE\_VAL** and its **float** and **long** double analogs, **HUGE\_VALF** and **HUGE\_VALL**, expand to expressions whose values are positive infinities; their evaluations raise no exceptions.<sup>14</sup>

Special cases for functions in <math.h> are covered directly or indirectly by the IEEE standard. X.3 identifies the functions that the IEEE standard specifies directly. The other functions in <math.h> treat infinities, NaNs, signed zeros, subnormals, and (provided the state for fenv\_access is on) the exception flags in a manner consistent with the basic arithmetic operations covered by the IEEE standard.

[The implementation need not honor the exception flags if the state for fenv\_access is off. For example, the translator could maintain a variable, say \_Fenv\_access, whose value would be nonzero just when the state for fenv\_access is on. Then a header could define macros in the style

<sup>13 0 - 0</sup> yields -0 instead of +0 just when the rounding direction is downward.

<sup>14</sup> HUGE\_VAL could not be implemented as

<sup>#</sup>define HUGE\_VAL (1.0/0.0) whose use raises the divide-by-zero exception.

```
#define foo(x) (_Fenv_access?_Foo(x):_FastFoo(x))
]
```

The invalid and divide-by-zero exceptions are raised as specified in subsequent subclauses of this annex.

The overflow exception is raised whenever an infinity—or, because of rounding direction, a maximal-magnitude finite number—is returned in lieu of a value whose magnitude is too large.

The underflow exception is raised whenever a result is tiny (essentially subnormal or zero) and suffers loss of accuracy. 15

Whether or when the trigonometric, hyperbolic, base-e exponential, base-e logarithmic, error, and lgamma functions raise the inexact exception is implementation-defined. For other functions, the inexact exception is raised whenever the rounded result is not identical to the mathematical result.

[The functions exempted from careful handling of the inexact flag are almost always inexact. Presumably there is little value in testing whether they are inexact.]

Whether the inexact exception may be raised when the rounded result actually does equal the mathematical result is implementation-defined. Whether the underflow (and inexact) exception may be raised when a result is tiny but not inexact is implementation-defined. Otherwise, as implied by X.7.6, the <math.h> functions do not raise spurious exceptions (detectable by the user).

[For some functions, for example pow, determining exactness in all cases might be too costly.]

Whether the functions honor the rounding direction mode is implementation-defined.

[Although correct rounding for transcendentals is desirable, costs may be prohibitive at this time.]

Generally, one-parameter functions of a NaN argument return that same NaN and raise no exception.

The specification in the following subclauses appends to the definitions in <math.h>.

# **X.9.1** Trigonometric functions

Type-face used in the following subclauses is misleading in that Courier bold is used for non-program elements. It will be cleaned up.

#### X.9.1.1 The acos function

- acos(1) returns +0.
- acos (x) returns a NaN and raises the invalid exception for |x| > 1.

<sup>15</sup> The IEEE standard allows different definitions of underflow. They all result in the same values, but differ on when the exception is raised.

<sup>16</sup> It is intended that undeserved underflow and inexact exceptions are raised only if determining inexactness would be too costly.

#### X.9.1.2 The asin function

- $asin(\pm 0)$  returns  $\pm 0$ .
- asin(x) returns a NaN and raises the invalid exception for |x| > 1.

#### X.9.1.3 The atan function

- atan( $\pm 0$ ) returns  $\pm 0$ .
- atan ( $\pm$ INFINITY) returns  $\pm \pi/2$ .

#### X.9.1.4 The atan2 function

- If one argument is a NaN then atan2 returns that same NaN; if both arguments are NaNs then atan2 returns one of its arguments.
- atan2 ( $\pm 0$ , x) returns  $\pm 0$ , for x > 0.
- atan2( $\pm 0$ , +0) returns  $\pm 0.17$
- atan2 ( $\pm 0$ , x) returns  $\pm \pi$ , for x < 0.
- atan2 ( $\pm 0$ , -0) returns  $\pm \pi$ .
- atan2(y,  $\pm 0$ ) returns  $\pi/2$  for y > 0.
- atan2(y,  $\pm 0$ ) returns  $-\pi/2$  for y < 0.
- atan2( $\pm y$ , INFINITY) returns  $\pm 0$ , for finite y > 0.
- atan2 ( $\pm$ INFINITY, x) returns  $\pm \pi/2$ , for finite x.
- atan2 ( $\pm y$ , -INFINITY) returns  $\pm \pi$ , for finite y > 0.
- atan2 ( $\pm$ INFINITY, INFINITY) returns  $\pm \pi/4$ .
- atan2 ( $\pm$ INFINITY, -INFINITY) returns  $\pm 3\pi/4$ .

[The more contentious cases are y and x both infinite or both zeros. See [7] for a justification of the choices above.]

#### X.9.1.5 The cos function

• cos (±INFINITY) returns a NaN and raises the invalid exception.

#### X.9.1.6 The sin function

- $sin(\pm 0)$  returns  $\pm 0$ .
- sin(±infinity) returns a NaN and raises the invalid exception.

#### X.9.1.7 The tan function

- $tan(\pm 0)$  returns  $\pm 0$ .
- tan (±INFINITY) returns a NaN and raises the invalid exception.

# **X.9.2** Hyperbolic functions

## X.9.2.1 The acosh function

- acosh(1) returns +0.
- acosh(+INFINITY) returns +INFINITY.

<sup>17</sup> atan2(0, 0) does not raise the invalid exception, nor does atan2(y, 0) raise the divide-by-zero exception.

• acosh(x) returns a NaN and raises the invalid exception if x < 1.

# X.9.2.2 The asinh function

- asinh ( $\pm 0$ ) returns  $\pm 0$ .
- asinh(±INFINITY) returns ±INFINITY.

# X.9.2.3 The atanh function

- atanh (±0) returns ±0.
- atanh (±1) returns ±INFINITY.
- atanh(x) returns a NaN and raises the invalid exception if |x| > 1.

#### X.9.2.4 The cosh function

• cosh (±INFINITY) returns +INFINITY.

#### X.9.2.5 The sinh function

- $sinh(\pm 0)$  returns  $\pm 0$ .
- sinh(±INFINITY) returns ±INFINITY.

#### X.9.2.6 The tanh function

- tanh (±0) returns ±0.
- tanh (±INFINITY) returns ±1.

# X.9.3 Exponential and logarithmic functions

#### X.9.3.1 The exp function was admirable and assist him and a section and a page 1

- exp(+infinity) returns +infinity. XXIXIXXII & SYMMER (XYIXIXXIX) of the first of
- exp(-INFINITY) returns +0.

#### X.9.3.2 The exp2 function

- exp2(+INFINITY) returns +INFINITY. Mayor and social base Mayor a parameter as a section
- exp2(-INFINITY) returns +0.

#### X.9.3.3 The expm1 function

- expm1 ( $\pm 0$ ) returns  $\pm 0$ .
- expm1(+INFINITY) returns +INFINITY
- expm1(-INFINITY) returns -1.

# **X.9.3.4** The frexp function (ISO 7.5.4.2)

- frexp(±0, exp) returns ±0, and returns 0 in \*exp.
- frexp(±INFINITY, exp) returns ±INFINITY, and returns an unspecified value in \*exp.
- frexp(x, exp) returns x if x is a NaN, and stores an unspecified value in \*exp.
- Otherwise, frexp raises no exception.

On a binary system, frexp is equivalent to the comma expression

```
( (*exp = (value == 0) ? 0 : (int)(1 + logb(value))),
    scalb(value, -(*exp)) )
```

## X.9.3.5 The ldexp function

On a binary system, 1dexp(x, exp) is equivalent to

```
scalb(x, exp)
```

[Note that ldexp may not provide the full functionality of scalb for extended values, because the power required to scale from the smallest (subnormal) to the largest extended value exceeds the minimum INT\_MAX allowed by Standard C.]

#### X.9.3.6 The log function

- $log(\pm 0)$  returns -INFINITY and raises the divide-by-zero exception.
- log(x) returns a NaN and raises the invalid exception if x < 0.
- log(+INFINITY) returns +INFINITY.

## X.9.3.7 The log10 function

- $log10 (\pm 0)$  returns -INFINITY and raises the divide-by-zero exception.
- log10 (x) returns a NaN and raises the invalid exception if x < 0.
- log10(+INFINITY) returns +INFINITY.

#### X.9.3.8 The log1p function

- $log1p(\pm 0)$  returns  $\pm 0$ .
- loglp(-1) returns -INFINITY and raises the divide-by-zero exception.
- loglp(x) returns a NaN and raises the invalid exception if x < -1.
- log1p(+INFINITY) returns +INFINITY.

#### X.9.3.9 The log2 function

- log2 (±0) returns -INFINITY and raises the divide-by-zero exception.
- log2(x) returns a NaN and raises the invalid exception if x < 0.
- log2(+INFINITY) returns +INFINITY.

#### X.9.3.10 The logb function

- logb(±INFINITY) returns +INFINITY.
- logb(±0) returns -infinity and raises the divide-by-zero exception.

#### X.9.3.11 The modf functions

- modf(value, iptr) returns a result with the same sign as the argument value.
- modf (±INFINITY, iptr) returns ±0 and stores ±INFINITY through iptr.
- modf of a NaN argument returns that same NaN and also stores it through iptr.

modf behaves as though implemented by

```
#include <math.h>
#include <fenv.h>
fenv_access_on
double modf(double value, double *iptr)
{
   int save_round = fegetround();
   fesetround(FE_TOWARDZERO);
   *iptr = nearbyint(value);
   fesetround(save_round);
   return copysign(
        (fabs(value) == INFINITY) ? 0.0 : value - (*iptr),
        value);
}
```

#### X.9.3.12 The scalb function

• scalb(x, n) returns x if x is infinite, zero, or a NaN.

# X.9.4 Power and absolute value functions

# X.9.4.1 The fabs function (ISO 7.5.6.2)

- fabs  $(\pm 0)$  returns +0.
- fabs(±INFINITY) returns +INFINITY.

#### X.9.4.2 The hypot function

- hypot(x, y), hypot(y, x), and hypot(x, -y) are equivalent.
- hypot(x, y) returns +INFINITY if x is infinite.
- If both arguments are NaNs then hypot returns one of its arguments; otherwise, if x is a NaN and y in not infinite then hypot returns that same NaN.
- hypot(x, ±0) is equivalent to fabs(x).

[hypot(INFINITY, NAN) returns + INFINITY, under the justification that hypot(INFINITY, y) is + INFINITY for any numeric value y.]

#### X.9.4.3 The pow function will leave a series bas MaM a series a great series as a series of the power of the

```
pow(x, ±0) returns 1 for any x.
pow(x, +INFINITY) returns +INFINITY for |x| > 1.
pow(x, +INFINITY) returns +0 for |x| < 1.</li>
pow(x, -INFINITY) returns +0 for |x| > 1.
pow(x, -INFINITY) returns +INFINITY for |x| < 1.</li>
pow(+INFINITY, y) returns +INFINITY for y > 0.
pow(+INFINITY, y) returns +0 for y < 0.</li>
pow(-INFINITY, y) returns -INFINITY for y an odd integer > 0.
pow(-INFINITY, y) returns +INFINITY for y > 0 and not an odd integer.
pow(-INFINITY, y) returns -0 for y an odd integer < 0.</li>
pow(-INFINITY, y) returns +0 for y < 0 and not an odd integer.</li>
pow(x, y) returns one of its NaN arguments if y is a NaN, or if x is a NaN and y is nonzero.
```

• pow(±1, ±INFINITY) returns a NaN and raises the invalid exception.

• pow(x, y) returns a NaN and raises the invalid exception for finite x < 0 and finite nonintegral y.

- pow(±0, y) returns ±infinity and raises the divide-by-zero exception for y an odd integer < 0.
- pow( $\pm 0$ , y) returns +INFINITY and raises the divide-by-zero exception for y < 0, finite, and not an odd integer.
- pow( $\pm 0$ , y) returns  $\pm 0$  for y an odd integer > 0.
- pow( $\pm 0$ , y) returns +0 for y > 0 and not an odd integer.

[See [7].

pow(NaN, 0). [7] provides extensive justification for the value 1. An opposing point of view is that any return value of a function of a NaN argument should be a NaN, even if the function is independent of that argument—as, in the IEEE standard, NAN <= +INFINITY is false and raises the invalid exception.]

## X.9.4.4 The sqrt function

sqrt is fully specified as a basic arithmetic operation in the IEEE standard.

# X.9.5 Error and gamma functions

#### X.9.5.1 The erf function

- erf ( $\pm 0$ ) returns  $\pm 0$ .
- erf (±INFINITY) returns ±1.

#### X.9.5.2 The erfc function

- erfc(+INFINITY) returns +0.
- erfc(-INFINITY) returns 2.

#### X.9.5.3 The gamma function

- gamma (+INFINITY) returns +INFINITY.
- gamma (x) returns a NaN and raises the invalid exception if x is a negative integer or zero.
- gamma (-INFINITY) returns a NaN and raises the invalid exception.

## X.9.5.4 The Igamma function

- lgamma(+INFINITY) returns +INFINITY.
- lgamma(x) returns +INFINITY and raises the divide-by-zero exception if x is a negative integer or zero.
- 1gamma (-INFINITY) returns a NaN and raises the invalid exception.

# X.9.6 Nearest integer functions

#### **X.9.6.1** The ceil function (ISO 7.5.6.1)

• ceil(x) returns x if x is  $\pm$  infinity or  $\pm$ 0.

The double version of ceil behaves as though implemented by

```
#include <math.h>
#include <fenv.h>
fenv_access_on
double ceil(double x)
{
    double result;
    int save_round = fegetround();
    fesetround(FE_UPWARD);
    result = rint(x); /* or nearbyint instead of rint */
    fesetround(save_round);
    return result;
}
```

#### X.9.6.2 The floor function

• floor(x) returns x if x is  $\pm$ INFINITY or  $\pm$ 0.

See the sample implementation for coil in X.9.6.1.

# X.9.6.3 The nearbyint function

The nearbyint function differs from the rint function only in that the nearbyint function does not raise the inexact flag.

# X.9.6.4 The rint function

The rint function use IEEE standard rounding according to the current rounding direction. It raises the inexact exception if its argument differs in value from its result.

#### X.9.6.5 The rinttol function

The rinttol function provides floating-to-integer conversion as prescribed by the IEEE standard. It rounds according to the current rounding direction. If the rounded value is outside the range of long int, the numeric result is unspecified and the invalid exception is raised. When it raises no other exception and its argument differs from its result, rinttol raises the inexact exception.

#### X.9.6.6 The round function

The double version of round behaves as though implemented by

```
#include <math.h>
#include <fenv.h>
fenv_access_on
double round(double x)
{
    double result;
    fenv_t save_env;
    feholdexcept(&save_env);
    result = rint(x);
    if (fetestexcept(FE_INEXACT))
    {
        fesetround(FE_TOWARDZERO);
        result = rint(copysign(0.5 + fabs(x), x));
    }
    feupdateenv(&save_env);
    return result;
}
```

The round function may but is not required to raise the inexact exception for nonintegral numeric arguments, as this implementation does.

# X.9.6.7 The roundtol function

The roundtol function differs from rinttol with the default rounding direction just in that roundtol (1) rounds halfway cases away from zero and (2) may but need not raise the inexact exception for nonintegral arguments that round to within the range of long int.

#### X.9.6.8 The trunc function

The trunc function uses IEEE standard rounding toward zero (regardless of the current rounding direction).

#### X.9.7 Remainder functions

#### X.9.7.1 The fmod function

- If one argument is a NaN then fmod returns that same NaN; if both arguments are NaNs then fmod returns one of its arguments.
- fmod  $(\pm 0, y)$  returns  $\pm 0$  if y is not zero.
- fmod(x, y) returns a NaN and raises the invalid exception if x is infinite or y is zero.
- fmod(x, ±INFINITY) returns x if x is not infinite.

The double version of fmod behaves as though implemented by

```
#include <math.h>
#include <fenv.h>
fenv_access_on
double fmod(double x, double y)
{
    double result;
    result = remainder(fabs(x), (y = fabs(y)));
    if (signbit(result)) result += y;
    return copysign(result, x);
}
```

#### X.9.7.2 The remainder function

The remainder function is fully specified as a basic arithmetic operation in the IEEE standard.

## X.9.7.3 The remquo function

The remquo function follows the specification for remainder. It has no further specification special to IEEE implementations.

# **X.9.8** Manipulation functions

## X.9.8.1 The copysign function

copysign is specified in the Appendix to the IEEE standard.

#### X.9.8.2 The nan functions

All IEEE implementations support quiet NaNs, in all floating formats.

#### X.9.8.3 The nextafter functions

- If one argument is a NaN then nextafter returns that same NaN; if both arguments are NaNs then nextafter returns one of its arguments.
- nextafter(x, y) raises the overflow and inexact exceptions if x is finite and the function value is infinite.
- nextafter(x, y) raises the underflow and inexact exceptions if the function value is subnormal and x != y.

# X.9.9 Maximum, minimum, and positive difference functions

#### X.9.9.1 The fdim function

• If one argument is a NaN then fdim returns that same NaN; if both arguments are NaNs then fdim returns one of its arguments.

#### X.9.9.2 The fmax function

• If just one argument is a NaN then fmax returns the other argument; if both arguments are NaNs then fmax returns one of its arguments.

The fmax function might be implemented as

```
{ return (isgreaterequal(x, y) || isnan(y)) ? x : y; }<sup>18</sup>
```

[Some applications might be better served by a max function that would return a NaN if one of its arguments were a NaN:

```
{ return (isgreaterequal(x, y) || isnan(x)) ? x : y; }
```

<sup>18</sup> Ideally, fmax would be sensitive to the sign of zero, for example fmax(-0.0, +0.0) would return +0; however, implementation in software might be impractical.

Note that two branches still are required, for symmetry in NaN cases.]

# X.9.9.3 The fmin function with placed a sa hallipoge with at notice of a same seems of T

fmin is analogous to fmax. See X.9.9.2.